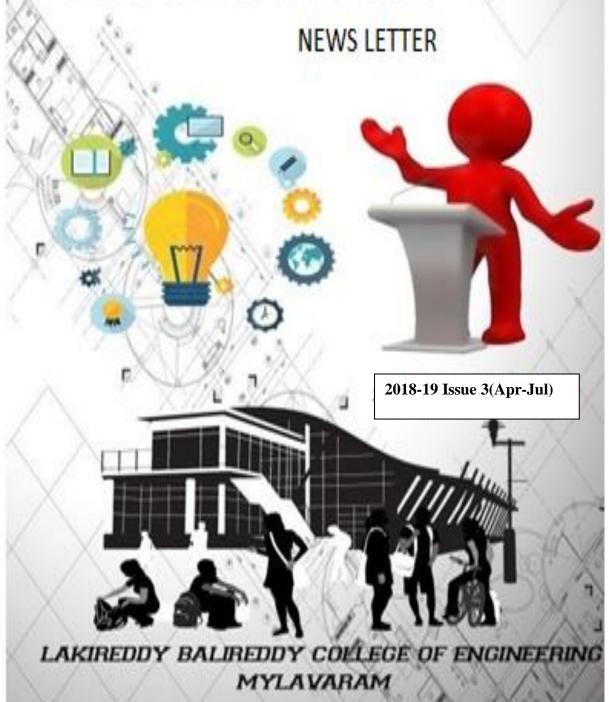
ELECTRONICS & COMMUNICATION ENGINEERING

CAMPUS PULSE





DEPARTMENT OF ELECTRONIC AND COMMUNICATION ENGINEERING

REPORT ON EVENT ASIC / FPGA DESIGN & ITS APPLICATIONS

Event Type: Faculty Development Programme

Date / Duration: 22nd – 27th April, 2019.

Resource Persons: 1. Mr. B. Nagendra, Sr. Application Engineer, Apply Volt 2. Mr. J. Pradeep, Sr. Application Engineer, Apply Volt

Name of the Coordinator: Dr. P. Lachi Reddy, Professor

Audience: ECE, EEE and EIE Faculty

Total Number of Participants: 59

Objective of the Event: To train the faculty on advanced VLSI Design tools

Outcome of the Event: The participants are able to

- 1. Design various devices using full custom and semi custom design methods.
- 2. Design the testable devices.
- 3. Implement the design using Zed boards and ZYNQ boards.

Description / Report on Event:

Day one of the FDP started with the Inauguration Function, in which Principal, HOD, Coordinator and other dignitaries presented their views on the importance and objective of conducting the FDP and motivated all the participants to effectively utilize all the sessions and gain practical knowledge.

The resource persons from Apply Volt have given the presentation on "Basics of ASIC Design and ASICs Vs FPGAs". And have shown the Design flow of Full Custom and Semi Custom ASICs.

During day two, the resource person conducted hands on session on schematic design from Verilog/DFT Netlist, Adding Netlist to schematic Netlist, Adding Libraries, Extraction of schematic and analysis. In lab session, resource person given hands on experience on physical design, floor planning, placement & routing, Physical verification using industry standard caliber Tool.

Day three started with demonstration on Full custom design flow by resource persons with hands on experience covering Schematic entry, Spice models and simulation using the mentor graphics tools.

During day four, resource persons conducted the hands on training on Vivado tool. Design flow using Vivado tool and implementation of the design on Zed board and ZYNQ boards.

Day five and six, completely for lab sessions on the tools learned during previous four days.

Feedback / Suggestions:

- 1. Good hospitality
- 2. More number of workshops/FDPs on similar advanced tools

Photographs:



Press Clippings:





DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

REPORT ON EVENT <<GUEST LECTURE>>

Event Type	by De Chapt	: Guest Lecture on "MEMS Fabrication & Applications" Organized partment of Electronics & Communication Engineering and ISTE Student ter
Date / Duration	:	6 th July 2019, FN Session
Resource Person	: Hyder	Dr. P. S. Srinivas Babu, Founder and Director Sales, Davinci Electric, abad
Name of Coordinator	•	Dr. P. Lachi Reddy
Target Audience	:	V-Semester ECE & EIE Students
Total no of Participar	nts:	300
Objective of the even		After attending the Guest Lecture, students are able to do their Mini and Main ts and publishing the Papers in reputed Journals.
Outcome of event	: applic	Students have learned about the Fabrication of MEMS devices, and the ations of MEMS devices where they are used.

Feedback / Suggestions :

Photographs





Press Clippings



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మెలవరం: మెలవరం లకిరెడ్డి బాలిరెడ్డి ఇంజనీ రింగ్ కళాశాలలో ఈసీఈ విభాగంలో ఎంఈఎంఎస్ ఫ్యాబికేషన్ అండ్ అప్లికేషన్స్పే శనివారం అవగాహన సదస్సు జరిగింది. డాక్టర్ పీఎస్ శ్రీనివాస బాబు రిసోర్స్ పర్సన్గా వ్యవహ రించారు. ఈసీఈ ద్వితీయ, తృతీయ సంవ **మాట్లాడుతున్న డాక్టర్ శ్రీనివాస బాబు** త్సరం విద్యార్థులకు అవగాహన కల్పిస్తూ లకు సహకరిస్తాయన్నారు. కార్యక్రమంలో టానికి ఎంతగానో ఉపయోగపడతాయన్నారు. లు, విద్యార్థులు పాల్గొన్నారు. డాక్టర్ పి.లచ్చిరెడ్డి ఈ అంశాలు విద్యార్థులకు ప్రాజెక్టులు, పరిశోధన కో ఆర్థినేటర్గా వ్యవహరించారు.



ఎంఈఎంఎస్ టెక్నాలజీ, సిస్టమ్స్, ఆప్లికేషన్స్, కళాశాల ప్రిన్సిపల్ డాక్టర్ కె.అప్పారావు, డీస్ ఫ్యాబికేషన్లో లేయర్ డిపోసియేషన్, లిథోగ్రఫీ, ఆకడమిక్స్ డాక్టర్ ఈవీ కృష్ణారావు, ఈసీఈ ఎటాచ్చింగ్, ప్యాకేజీ టైప్స్, పరిశోధనలు చేయ విభాగాధిపతి డాక్టర్ వై.అమర్బాబు, అధ్యాపకు